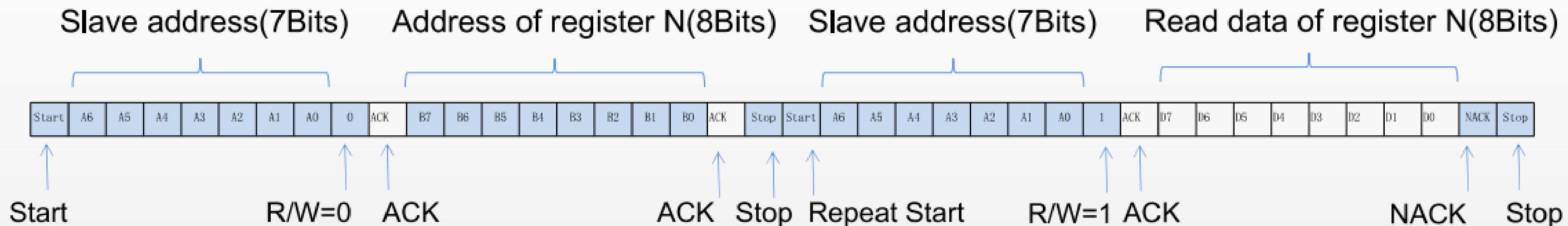


# I2C时序图/I2C Timing Diagram

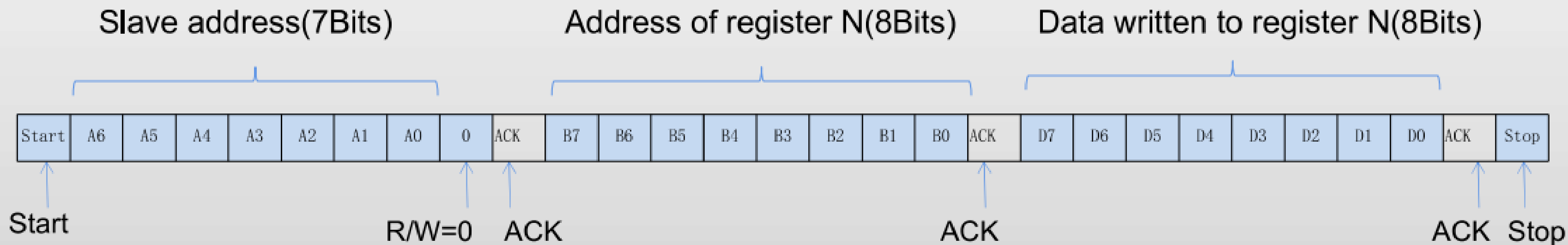
Master SDA

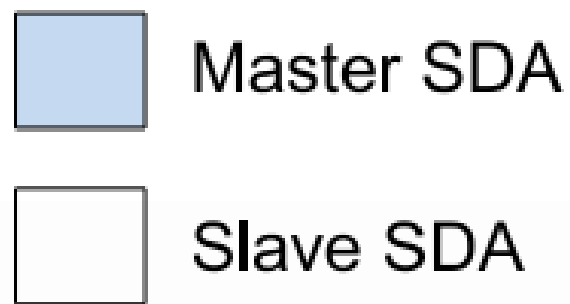
Slave SDA

## I2C Read:

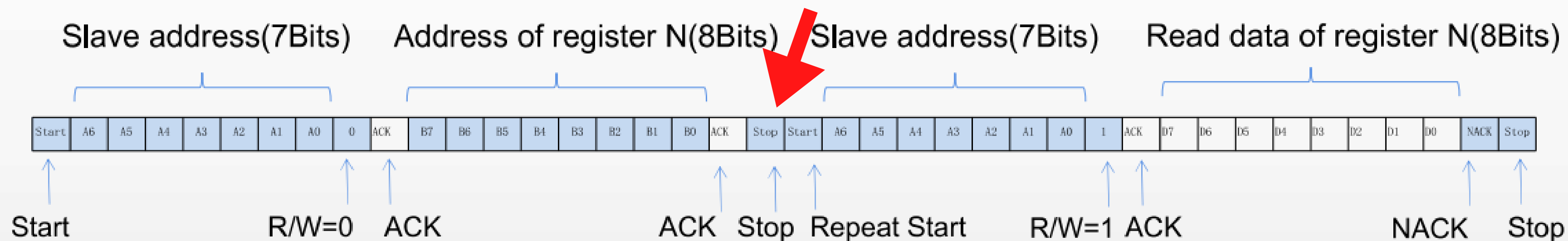


## I2C Write:



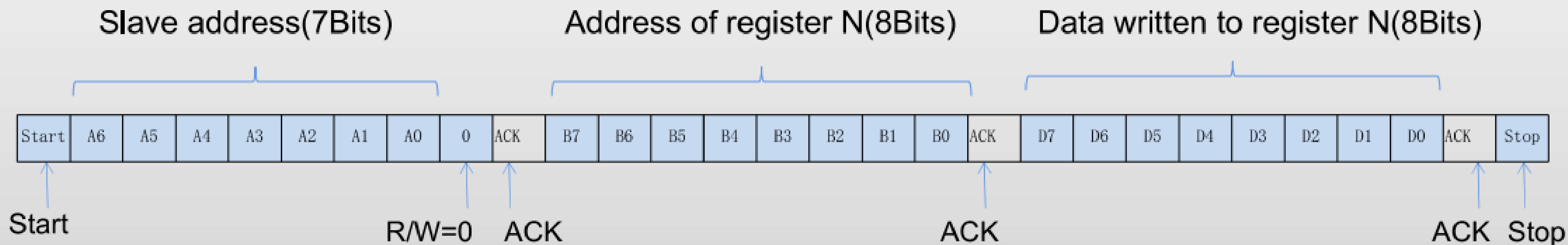


## I2C Read:

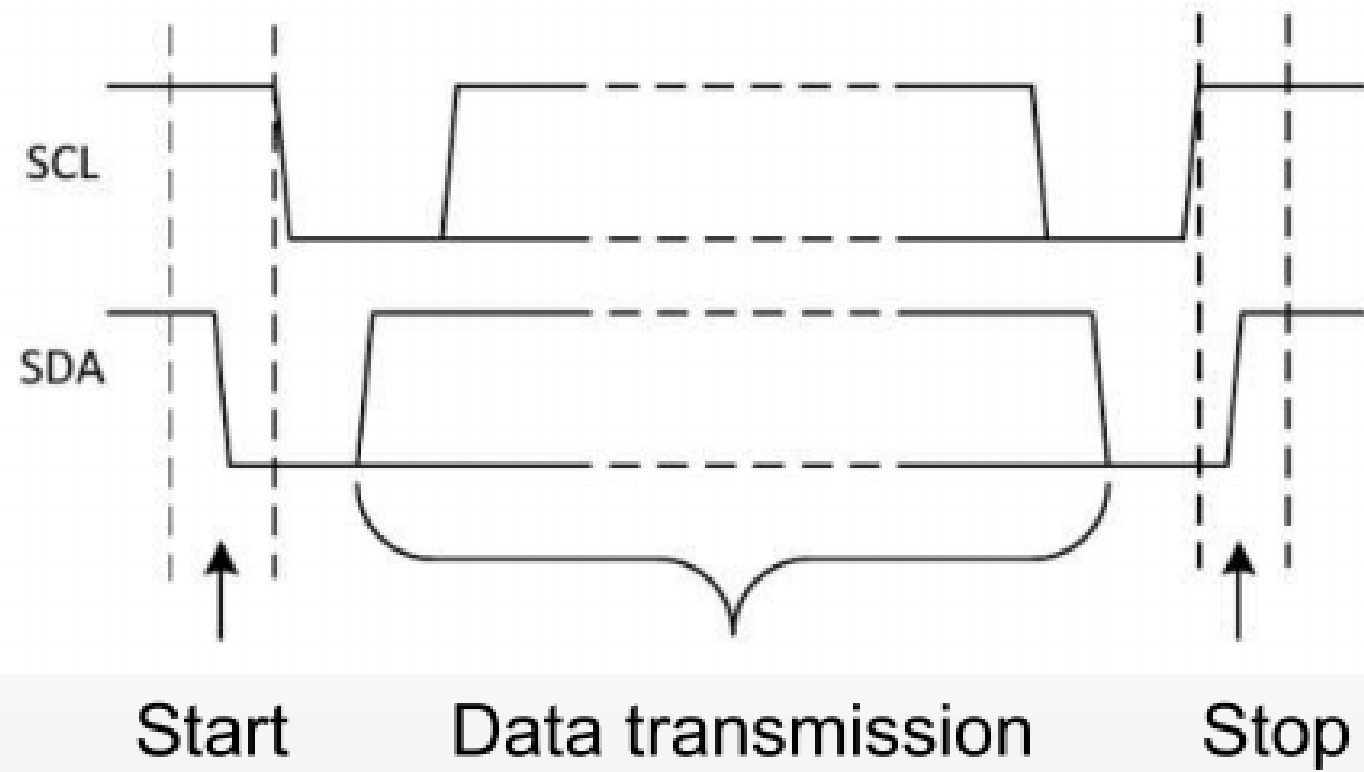


注意：I2C读取数据时，再次发送Start条件钱需要添加一个Stop条件  
 Compared to the standard I2C's ReStart, 8EnCoder has an additional stop bit,  
 which is identified as shown in the following figure:

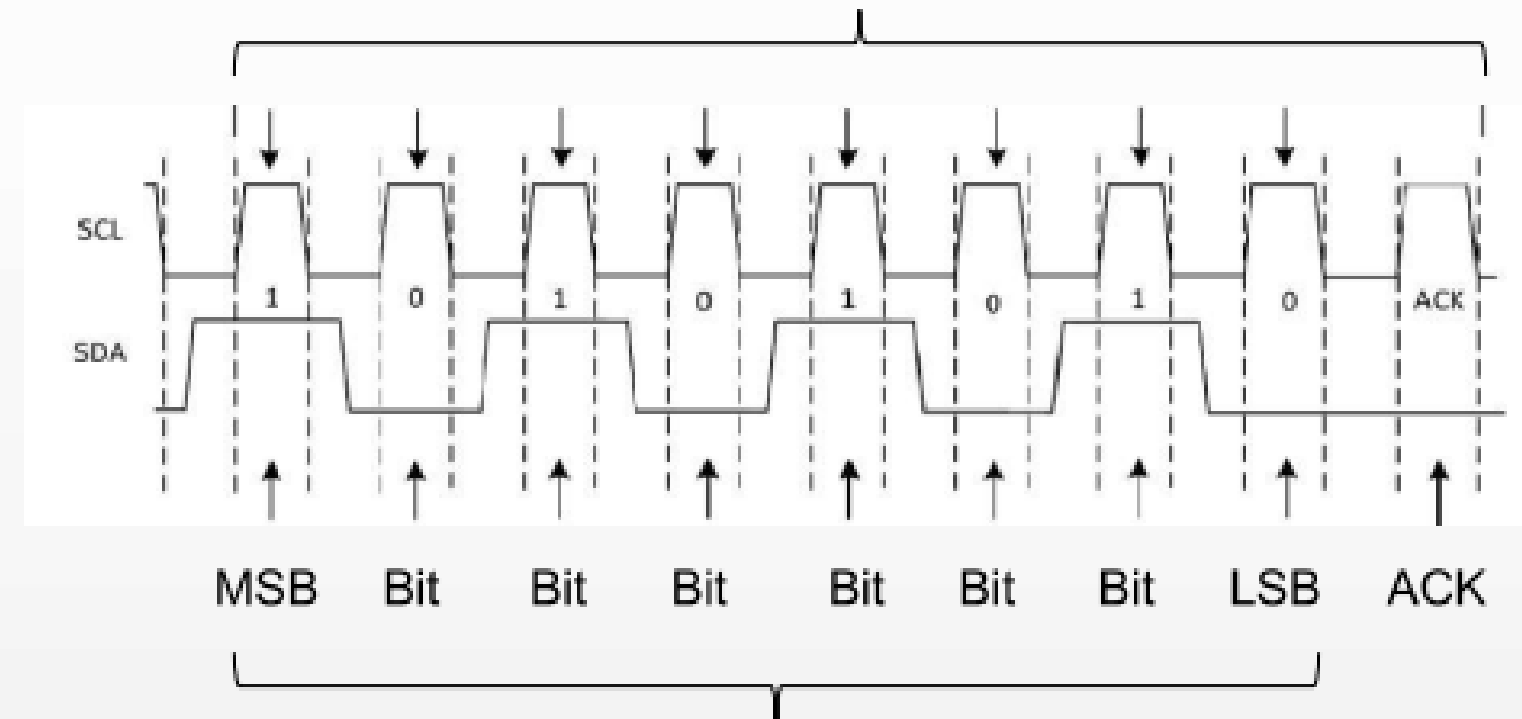
## I2C Write:



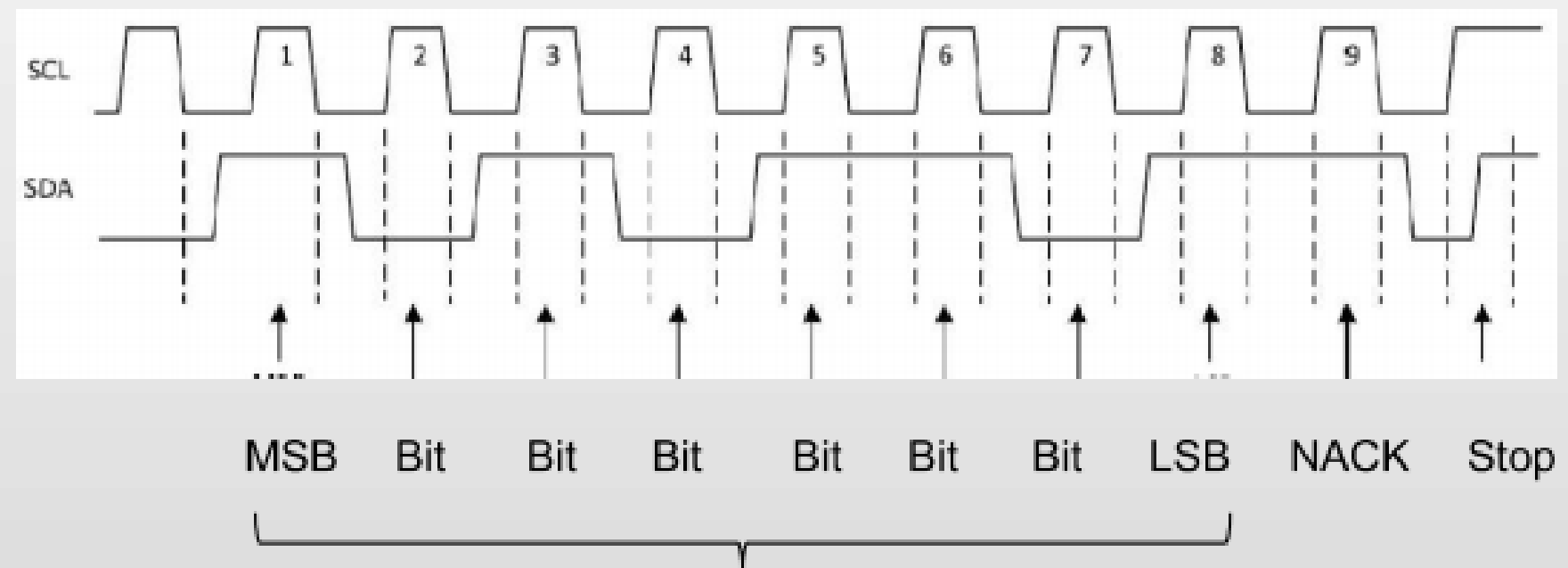
# Timing diagram:



When SCL is high, the data on SDA must remain stable



Data: 10101010(0xAAH)



Data: 10101101(0xADH)